

GIETPOLYTECHNIC, JAGATPUR, CUTTACK

LESSON PLAN

Discipline: Electrical Engg.	Semester:4 th	Name Of The Teaching Faculty: SUGANDHA PUSPITA MADHUJHARA
Subject: Analog Electronics & OP-AMP (Th2)	No. Of Days Per Week Class Allotted: 04P	Semester From Date:04.02.2025 To Date: 17.05.2025 No.ofweeks:15
Week	Class Day	Theory Topic
1 st week		P-N JUNCTION DIODE
	1 st	Introduction to semiconductor and its Application
	2 _{nd}	P-N Junction Diode and its working
	3rd	V-I characteristic of PN junction Diode.
	4th	DC load line
2 nd week	1st	Important terms such as Ideal Diode ,Knee voltage
	2nd	Junctions breakdown (Zener break down and Avalanche breakdown)
	3rd	P-N Diode clipping and Diode clamping Circuit.
		SPECIAL SEMICONDUCTOR DEVICES
	4 th	Thermistors, Sensors & barraters
	1st	Zener Diode and Tunnel Diode
3 rd week	2 _{nd}	PIN Diode
		RECTIFIER CIRCUITS & FILTERS
	3rd	Classification of rectifiers
	4 th	Analysis of half wave, full wave, centre tapped
4 th week	1st	Bridge rectifiers and calculate
	2 _{nd}	DC output current and voltage
	3rd	RMS output current and voltage
	4 th	Rectifier efficiency and Ripple factor
5 th week	1 st	Regulation
	2 nd	Transformer utilization factor
	3rd	Peak inverse voltage
	4 th	Filters
	1 st	Shunt capacitor filter

6 th week	2 nd	Choke input filter
	3rd	π filter
	Nach Land	TRANSISTORS
		Principle of Bipolar junction transistor
7 th week	1 st	Different modes of operation of transistor
	2 _{nd}	Current components in a transistor
	3rd	Transistors and amplifier
	4 th	Transistor circuit configuration & its characteristics
	1 st	CB CE CC Configuration
8 th week		TRANSISTOR CIRCUITS
	2nd	Transistor biasing
	3rd	Stabilization and Stability factor
		Different method of Transistors Biasing
	1 st	Base resistor method
9 th week	2nd	Collector to base bias
	3rd	Self bias or voltage divider method
		TRANSISTOR AMPLIFIERS & OSCILLATORS
	4 th	Practical circuit of transistor amplifier, DC load line and DC equivalent circuit
		Calculation of gain, Phase reversal, H-parameters of transistors
	1 st	
10 th week	2nd	Simplified H-parameters of transistors, Analysis of CB, CE, CC amplifie using generalized approximate model
	3rd	R.C coupled amplifier and Transformer coupled amplifier
	4 th	Feedback in amplifier
11 th week	1 st	General theory of feedback
	2 nd	Negative feedback circuit, Advantage of negative feedback
	3rd	Power amplifier and its classification
	4 th	Difference between voltage amplifier and power amplifier
12 th week	1 st	Transformer coupled class-A power amplifier
	2 nd	Class A push – pull amplifier, Class-B push –pull amplifier
	3rd	Types of oscillators and Essentials of transistor oscillator
	4 th	Principle of operation of tuned collector Hartley , colpitt

13 th week	1 st	Phaseshift, Wein-bridge oscillator
13 0000	2 nd	FIELD EFFECT TRANSISTOR
	3rd	Classification of FET Advantages of FET over BJT and Principle of operation of BJT
	4 th	FET parameters, DC drain resistance, AC drain resistance, Trans- conductance, Biasing of FET
		OPERATIONAL AMPLIFIERS
14 th week	1st	General circuit simple of OP-AMP and IC – CA – 741 OP-AMP
L-V V/CCK	2 nd	Operational amplifier stages
	3rd	- the standard control of appreciational amplifier
	4 th	Open loop OP-AMP configuration and OPAMP with reconstruction
		Inverting OP-AMP and Noninverting OP-AMP
15 th week		Voltage follower & buffer
	2nd	Differential amplifier
	3rd	A Librar summing amplifier
	4 th	Subtractor, integrator, differentiator and comparator

SIGN OF FACULTY

SIGN OF SR.LECT,
Head of Dept. (HOD)
Electrical & ETC Fair
G.' E.T (1 OLY),

SIGN OF PRINCIPAL